

11/17/03

NOV 17 2003
PATENT & TRADE MARK OFFICE

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention	High-Voltage Transistor With A Multi-Layer Conduction Region						
Application Number:	09/961229 						
Confirmation Number:	4375						
First Named Applicant:	Vladimir Rumennik						
Attorney Docket Number:	03692.P007XD4						
Art Unit:	2811						
Examiner:	Shouxiang Hu						
Search string:	(6633065 or 6639277).pn.						
(ELECTRONIC DOCUMENT CENTER 2000)							
RECEIVED NOV 28 2003							
USPTO-GOV/2000							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<i>Sh</i>	1	6633065	2003-10-14	Rumennik et al.	B2		
<i>Sh</i>	2	6639277	2003-10-28	Rumennik et al.	B2		
Remarks							
Note: Remarks are not for responding to an office action.							
This information disclosure statement is being submitted concurrent with the filing of a Request for Continued Examination in the above-referenced application.							
Signature							
Examiner Name				Date			
<i>Shouxiang Hu</i>				6/28/04			



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Art Unit:	2811						
Examiner:	Shouxiang Hu.						
Search string:	(4614959 or 5294824).pn.						
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
Init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
SH	1	4614959	1986-09-01	Nakagawa			
SH	2	5294824	1994-03-01	Okada			
Signature							
Examiner Name				Date			
Shouxiang Hu				6/28/04			

